REMARKS

Claims 1 and 4 are currently pending. Claims 2, 3, and 5-15 are cancelled without prejudice. Continued Examination is hereby requested.

Claims 1 was rejected under 35 U.S.C. 102(e) as being anticipated by Carlsgaard and under 35 U.S.C. 103(a) as being obvious from the combination of Reitmeier and Kimball.

Claim 1 is amended to include among other limitations, "writing the interlaced frame to a frame buffer after decompressing the frame".

Claim 3, prior to this amendment recited, among other limitations, "decompressing the frame, thereby resulting in the interlaced frame". Examiner indicated that "Reitmeier discloses the use of decoding MPEG signals including the MPEG-2 signals meeting the claimed decompression limitations". FOA, at 3. Examiner also indicated that Kimball "discloses this convention practice (Fig. 9) wherein a memory 102 includes a TS presentation buffer 130". FOA at 3.

However, Kimball, Figure 9 clearly shows that the TS presentation buffer 130 is prior to the "MPEG Video Decoder 132". Therefore, Kimball does not teach or fairly suggest "writing the interlaced frame to a frame buffer after decompressing the frame" (claim 1) and "a frame buffer for storing the interlaced frames after the interlaced frames are decoded" (claim 4).

Examiner has indicated that "Reitemeier does disclose a MPEG transport Demux 108 (Figs. 1-2), which is received from a tuner 104 (television source)". However, Examiner

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does refer to "a buffer for storing a received signal (i.e. interlaced or progressive) which is subsequently transport stream demultiplexed…".

Even if Reitmeier were to disclose "a buffer for storing a received signal (i.e., interlaced or progressive) which is subsequently transport stream demultiplexed", Reitmeier does not teach or fairly suggest "writing the interlaced frame to a frame buffer after decompressing the frame" (claim 1) and "a frame buffer for storing the interlaced frames after the interlaced frames are decoded" (claim 4).

Assignee calls Examiner's attention to Figure 1 which clearly show that the "Transport Demux 108" occurs prior to "video decoder 120". "[A] buffer for storing a received signal … which is subsequently transport stream demultiplexed" would not be "writing the interlaced frame to a frame buffer after decompressing the frame" (claim 1) or "a frame buffer for storing the interlaced frames after the interlaced frames are decoded" (claim 4).
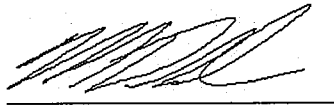
Accordingly, Examiner is requested to withdraw the rejection to claims 1 and 4.

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Conclusion

For at least the foregoing reasons, each of the pending claims are allowable, thereby placing the application in a condition for allowance. A notice of allowance is respectfully requested.

The Commissioner is hereby authorized to charge any deficiency or credit any overpayment related to this action to Deposit Account No. 13-0017.

Respectfully Submitted,

Date: February 12, 2007

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